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24309 7590 04/26/2006

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Pat Tompkins	(Depositor's name)
<i>Pat Tompkins</i>	(Signature)
June 28, 2006	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/769,663	01/29/2004	Soon-Shin Chee	X-1436 US	8545

TITLE OF INVENTION: INTEGRATED CIRCUIT AND METHOD OF PRODUCING A CARRIER WAFER FOR AN INTEGRATED CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	07/26/2006
EXAMINER	ART UNIT	CLASS-SUBCLASS			
ZARNEKE, DAVID A	2891	438-108000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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1 John J. King
2 Kim Kanzaki
3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

XILINX, INC.

San Jose, California 95124

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature

Typed or printed name

Keith A. Chanroo

Date June 28, 2006

Registration No. 36.480

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COMMISSIONER FOR PATENTS
P. O. Box 1450
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Inventor(s): Soon-Shin Chee et al.

Assignee: XILINX, INC.

Serial No.: 10/769,663

Filed: January 29, 2004

Title: Integrated Circuit and Method of Producing a Carrier Wafer for an Integrated Circuit

Docket No.: X-1436 US

Enclosed: Return Receipt Postcard

Fee Transmittal - Part B

Date: June 28, 2006

VIA FACSIMILE 571-273-2885

Atty/Sec: KAC/pat



conf.no. 8545